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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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Sheet 1

of 4

Complete if Known

Application Number	10/699,116
Filing Date	October 31, 2003
First Named Inventor	Donnelly
Art Unit	2187
Examiner Name	Chace, C.
Attorney Docket Number	RAMB-01065US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-5742840	04-21-1998	Hansen et al.	
		US-5748872	05-05-1998	Norman	
		US-5860080	01-12-1999	James et al.	
		US-5867422	02-02-1999	John	
		US-5898863	04-27-1999	Ofer et al.	
		US-6006318	12-21-1999	Hansen et al.	
		US-6034878	03-07-2000	Osaka et al.	
		US-6038682	03-14-2000	Norman	
		US-6092229	07-18-2000	Boyle et al.	
		US-6125419	09-26-2000	Umemura et al.	
		US-6151648	11-21-2000	Haq	
		US-6154826	11-28-2000	Wulf et al.	
		US-6160423	12-12-2000	Haq	
		US-6255859	07-03-2001	Haq	
		US-6263413	07-17-2001	Motomura et al.	
		US-6327205	12-04-2001	Haq	
		US-6408402	06-18-2002	Norman	
		US-6487102	11-26-2002	Halbert et al.	

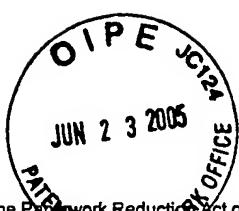
FOREIGN PATENT DOCUMENTS

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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				First Named Inventor	Donnelly
				Art Unit	2187
				Examiner Name	Chace, C.
Sheet	3	of	4	Attorney Docket Number	RAMB-01065US1

NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. ¹	
		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. ^{T²}
		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/714,025, filed 11-14-2003 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/883,474, filed 06-30-2004 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/882,999, filed 06-30-2004 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/859,438, filed 05-31-2004 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/858,850, filed 05-31-2004 by Pete D. Vogt
		*U.S. Patent Application Serial No. 10/859,060, filed 05-31-2004 by Pete D. Vogt
		NG, R. (Sun Microsystems Inc.), "Fast computer memories", IEEE Spectrum, October 1992, pp. 36-39
		SALTERS, R.H.W. (Philips Research Laboratories), "Fast DRAMS for sharper TV", IEEE Spectrum, October 1992, pp. 40-42

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				<i>First Named Inventor</i>	Donnelly
				<i>Art Unit</i>	2187
				<i>Examiner Name</i>	Chace, C.
Sheet	4	of	4	<i>Attorney Docket Number</i>	RAMB-01065US1
<i>(Use as many sheets as necessary)</i>					

NON-PATENT LITERATURE DOCUMENTS

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Art Unit	2187
Examiner Name	Chace, C.

Attorney Docket Number RAMB-01065US1

U. S. PATENT DOCUMENTS

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		Number-Kind Code ² (if known)			
		US-6016282	1-18-2000	Keeth	
		US-6449727	09-10-2002	Toda	
		US-4644532	02-17-1987	George et al.	
		US-4667305	05-19-1987	Dill et al.	
		US-4747100	05-24-1988	Roach et al.	
		US-4864563	09-05-1989	Pavey et al.	
		US-5228134	07-13-1993	MacWilliams et al.	
		US-5355467	10-11-1994	MacWilliams et al.	
		US-5408646	04-18-1995	Olnowich et al.	
		US-5537394	07-16-1996	Abe et al.	
		US-5544342	08-06-1996	Dean	
		US-5553266	09-03-1996	Metzger et al.	
		US-5557266	09-17-1996	Calvignac et al.	
		US-5604735	02-18-1997	Levinson et al.	
		US-5630095	05-13-1997	Snyder	
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		US-5787083	07-28-1998	Iwamoto et al.	
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 99/30240	06-17-1999	Intel Corporation		
		WO 99/41666	08-19-1999	Intel Corporation		
		WO 99/41667	08-19-1999	Intel Corporation		
		EP 1069509 A2	01-17-2001	Texas Instruments Incorporated		

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		Number-Kind Code ² (if known)			
		US-5802565	09-01-1998	McBride	
		US-5805798	09-08-1998	Kearns et al.	
		US-5848145	12-08-1998	Gallagher et al.	
		US-5884036	03-16-1999	Haley	
		US-5893921	04-13-1999	Bucher et al.	
		US-5900017	05-04-1999	Genduso et al.	
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		US-5953215	09-14-1999	Karabatsos et al.	
		US-6142830	11-07-2000	Looeffler	
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		US-6317352	11-13-2001	Halbert et al.	
		US-6414904	07-02-2002	So et al.	
		US-6446158	09-03-2002	Karabatsos	

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		US-6493250	12-10-2002	Halbert et al.	
		US-6530006	03-04-2003	Dodd et al.	
		US-6449679	09-10-2002	Ryan et al.	
		US-6108731	08-22-2000	Suzuki et al.	
		US-5701313	12-23-1997	Purdham	
		US-5513135	04-30-1996	Dell et al.	
		US-6065092	05-16-2000	Roy	
		US-5034917	07-23-1991	Bland et al.	
		US-4977498	12-11-1990	Rastegar et al.	
		US-5068650	11-26-1991	Fernandez et al.	
		US-4947257	08-07-1990	Fernandez et al.	
		US-6643752	11-04-2003	Donnelly et al.	
		US-6690726	02-10-2004	Yavits et al.	
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		US-6104417	08-15-2000	Nielsen et al.	
		US-6208273	03-27-2001	Dye et al.	
		US-5883839	03-16-1999	Tosaka et al.	

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		US-20040260991	12-23-2004	Vogt et al.	
		US-20040250181	12-09-2004	Vogt et al.	
		US-20040250153	12-09-2004	Vogt	
		US-20040150024	12-09-2004	Vogt	
		US-20040246786	12-09-2004	Vogt	
		US-20040246785	12-09-2004	Vogt	
		US-20040246767	12-09-2004	Vogt.	
		US-20040015650	01-22-2004	Zumkehr et al.	
		US-20030177313	09-18-2003	Iyer et al.	
		US-20020083287	06-27-2002	Zumkehr et al.	
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Sheet	5	of	7	Attorney Docket Number	RAMB-01065US1

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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		QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
		KARABATSOS, C., "Quad Band Memory (QBM) Technology", Kentron Technologies, Inc., April 2001	
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		"Intel 82804AA Memory Repeater Hub for SDRAM (MRH-S) Datasheet", Intel Corp., pp. 2-48 (Nov. 1999)	
		"Intel 82805AA Memory Translater Hub (MTH) Datasheet", Intel Corp., pp. 1-48 (Nov. 1999)	
		CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)	
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		JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				<i>Application Number</i>	10/699,116
<i>(Use as many sheets as necessary)</i>				<i>Filing Date</i>	October 31, 2003
				<i>First Named Inventor</i>	Donnelly
				<i>Art Unit</i>	2187
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		GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SDRAM Consortium, SDRAM Inc., pp. 1-14 (Aug. 1997)			
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		Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment "Y" of the presentation titled "SyncLink", May 24, 1995, New Orleans			
		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Server Memory Architecture: Capacity, Performance, Reliability, and Longevity", February 18, 2004			
		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004			

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		IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992		
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		GUSTAVSON et al., "Macintosh HD:Desktop Folder:SLDRAMrepeaterConcerns", Sunday, November 23, 1997, 4 pages		
		JEDDELOH, JOE, "Fully Buffered DIMM (FB-DIMM)", Advanced Systems Technology, Micron Technology, Inc. April 15-16, 2004		

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